

Amendments to the Specification:

Please replace the paragraph on page 13, lines 10-19 with the following amended paragraph:

--Further, the wide band modulation PLL according to the first embodiment comprises a frequency dividing ratio generation part 26 for generating a frequency dividing ratio set in the frequency divider 22 from phase modulation data and carrier frequency data inputted from the outside, an A/D converter 27 connected to the loop filter 25, a control signal generation part 28 for adjusting a modulation factor of modulation data while generating a control signal to the VCO 21 based on the phase modulation data and an output signal of the A/D converter 27, and a D/A converter 29 for making D/A conversion of the adjusted modulation data and outputting a control voltage V_{tm} to the control voltage terminal for modulation signal of the VCO 21 as an analog signal.--

Please replace the paragraph on page 17, lines 17-23 with the following amended paragraph:

--When N_1 satisfying the mathematical formula 4 is inputted to the frequency divider ~~[[26]]~~22, as a result of that, f_{VCO} is locked at the frequency f_1 . At this time, as shown by a point β_t of Fig. 4(a), a voltage applied to the control voltage terminal for PLL of the VCO 21 becomes $V_{t1}=V_{t11}$ and similarly, conversion into a digital value is made by the A/D converter 27 and V_{t11} is stored in the measurement result storage part 30 of the control signal generation part 28.--

Please replace the paragraph on page 19, lines 3-8 with the following amended paragraph:

--The control voltage V_{t1} of input to the control voltage terminal for PLL at this time is set at V_{t12} (a point γ_t in Fig. ~~[[4(b)]]~~4(a)). A value in which this V_{t12} is converted into a digital value by the A/D converter 27 is stored in the measurement result storage part 30 of the control signal generation part 28. At this time, a relation shown by a mathematical formula 6 holds between the modulation sensitivity K_m and the modulation sensitivity K_1 .--